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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Manpreet S. Khaira et al.

Title: LOGIC VERIFICATION IN LARGE SYSTEMS

Docket No.: 884.107US1

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Filed: July 2, 1999

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Examiner: Samarina Makhdoom

Group Art Unit: 2123

Commissioner for Patents  
Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ Response Under 37 CFR 1.111 (3 Pages).
- ☒ Exhibit A (24 pgs.) including:
  - Declaration Under 37 CFR § 1.131- Jeremy S. Casas, copy of signed invention disclosure
  - Declaration Under 37 CFR § 1.131- Erik M. Seligman, copy of signed invention disclosure
  - Declaration Under 37 CFR § 1.131- Honghua H. Yang, copy of signed invention disclosure

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Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

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By: *Danny J. Padys*  
Atty: Danny J. Padys  
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**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 27 day of November, 2002.

Jane Sagers  
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*Jane Sagers*  
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